CS 31: Introduction to Computer Systems

22: Race Conditions & Synchronization
April 23, 2020
Recap

• To speed up a job, must divide it across multiple cores.

• Thread: abstraction for execution within process.
  – Threads share process memory.
  – Threads may need to communicate to achieve goal

• Thread communication:
  – To solve task (e.g., neighbor GOL cells)
  – To prevent bad interactions (synchronization)
If one CPU core can run a program at a rate of X, how quickly will the program run on two cores? Why?

A. Slower than one core (<X)
B. The same speed (X)
C. Faster than one core, but not double (X-2X)
D. Twice as fast (2X)
E. More than twice as fast (>2X)
If one CPU core can run a program at a rate of X, how quickly will the program run on two cores? Why?

A. Slower than one core (<X) (if we try to parallelize serial applications!)
B. The same speed (X) (some applications are not parallelizable)
C. Faster than one core, but not double (X-2X): most of the time: (some communication overhead to coordinate/synchronization of the threads)
D. Twice as fast (2X) (class of problems called embarrassingly parallel programs. E.g. protein folding, SETI)
E. More than twice as fast (>2X) (rare: possible if you have more CPU + more memory)
Parallel Speedup

• Performance benefit of parallel threads depends on many factors:
  – algorithm divisibility
  – communication overhead
  – memory hierarchy and locality
  – implementation quality

• *For most programs*, more threads means more communication, diminishing returns.
They’re all executing the same program (shared instructions in text), though they may be at different points in the code.
Kernel-Level Threads

Kernel Context switching over threads

Each process has explicitly mapped regions for stacks
Synchronization

• Synchronize: to (arrange events to) happen such that two events do not overwrite each other’s work.

• Thread synchronization
  – When one thread has to wait for another
  – Events in threads that occur “at the same time”

• Uses of synchronization
  – Prevent race conditions
  – Wait for resources to become available (only one thread has access at any time - deadlocks)
Synchronization:
Too Much Milk (TMM)

<table>
<thead>
<tr>
<th>Time</th>
<th>You</th>
<th>Your Roommate</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.00</td>
<td>Arrive home</td>
<td></td>
</tr>
<tr>
<td>3.05</td>
<td>Look in fridge, no milk</td>
<td></td>
</tr>
<tr>
<td>3.10</td>
<td>Leave for the grocery store</td>
<td></td>
</tr>
<tr>
<td>3.15</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.20</td>
<td>Arrive at the grocery store</td>
<td></td>
</tr>
<tr>
<td>3.25</td>
<td>Buy Milk</td>
<td></td>
</tr>
<tr>
<td>3.30</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.35</td>
<td>Arrive home, put milk in fridge</td>
<td>Arrive Home</td>
</tr>
<tr>
<td>3.40</td>
<td>Look in fridge, find milk</td>
<td></td>
</tr>
<tr>
<td>3.45</td>
<td>Cold Coffee (nom)</td>
<td></td>
</tr>
</tbody>
</table>

What mechanisms do we need for two independent threads to communicate and get a consistent view (computer state)?
How many cartons of milk can we have in this scenario? (Can we ensure this somehow?)

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A. One carton (you)  
B. Two cartons  
C. No cartons  
D. Something else
### Synchronization: Too Much Milk (TMM): One possible scenario

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What mechanisms do we need for two independent threads to communicate and get a consistent view (computer state)?
**Synchronization:**

*Threads get scheduled in an arbitrary manner: bad things may happen: ...or nothing may happen*

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What mechanisms do we need for two independent threads to communicate and get a consistent view (computer state)?
Synchronization Example

- Coordination required:
  - Which thread goes first?
  - Threads in different regions must work together to compute new value for boundary cells.
  - Threads might not run at the same speed (depends on the OS scheduler). Can’t let one region get too far ahead.
  - Context switches can happen at any time!
Thread Ordering
(Why threads require care. Humans aren’t good at reasoning about this.)

• As a programmer you have *no idea* when threads will run. The OS schedules them, and the schedule will vary across runs.

• It might decide to context switch from one thread to another *at any time*.

• Your code must be prepared for this!
  – Ask yourself: “Would something bad happen if we context switched here?”

• hard to debug this problem if it is not reproducible
Example: The Credit/Debit Problem

• Say you have $1000 in your bank account
  – You deposit $100
  – You also withdraw $100

• How much should be in your account?

• What if your deposit and withdrawal occur at the same time, at different ATMs?
Credit/Debit Problem: Race Condition

Thread $T_0$

**Credit (int a)** {
  int b;
  
  b = ReadBalance ();
  b = b + a;
  WriteBalance (b);
  
  PrintReceipt (b);
} 

Thread $T_1$

**Debit (int a)** {
  int b;
  
  b = ReadBalance ();
  b = b - a;
  WriteBalance (b);
  
  PrintReceipt (b);
}
Credit/Debit Problem: Race Condition

Say T₀ runs first
Read $1000 into b

Thread T₀
Credit (int a) {
    int b;
    b = ReadBalance ();
    b = b + a;
    WriteBalance (b);
    PrintReceipt (b);
}

Thread T₁
Debit (int a) {
    int b;
    b = ReadBalance ();
    b = b - a;
    WriteBalance (b);
    PrintReceipt (b);
}
Credit/Debit Problem: Race Condition

**Thread T₀**

Credit (int a) {
    int b;
    b = ReadBalance ();
    b = b + a;
    WriteBalance (b);
    PrintReceipt (b);
}

**Thread T₁**

Debit (int a) {
    int b;
    b = ReadBalance ();
    b = b - a;
    WriteBalance (b);
    PrintReceipt (b);
}

Say T₀ runs first
Read $1000 into b
Switch to T₁
Read $1000 into b
Debit by $100
Write $900

CONTEXT SWITCH
Credit/Debit Problem: Race Condition

Thread T₀

Credit (int a) {
    int b;
    b = ReadBalance ();
    b = b + a;
    WriteBalance (b);
    PrintReceipt (b);
}

Thread T₁

Debit (int a) {
    int b;
    b = ReadBalance ();
    b = b - a;
    WriteBalance (b);
    PrintReceipt (b);
}

Say T₀ runs first
Read $1000 into b
Switch to T₁
Read $1000 into b
Debit by $100
Write $900

Switch back to T₀
Read $1000 into b
Credit $100
Write $1100

Bank gave you $100!

What went wrong?
“Critical Section”

Thread $T_0$

Credit (int a) {
    int b;
    b = ReadBalance ();
    b = b + a;
    WriteBalance (b);
    PrintReceipt (b);
}

Thread $T_1$

Debit (int a) {
    int b;
    b = ReadBalance ();
    b = b - a;
    WriteBalance (b);
    PrintReceipt (b);
}

Bank gave you $100!

What went wrong?

Danger Will Robinson!
To Avoid Race Conditions

1. Identify critical sections

2. Use synchronization to enforce mutual exclusion
   - Only one thread active in a critical section
Critical Section and Atomicity

• Sections of code executed by multiple threads
  – Access shared variables, often making local copy
  – Places where order of execution or thread interleaving will affect the outcome
  – Follows: read + modify + write of shared variable

• Must run atomically with respect to each other
  – Atomicity: runs as an entire instruction or not at all. Cannot be divided into smaller parts.
Which code region is a critical section?

Thread A

```c
main ()
{
    int a,b;

    a = getShared();
    b = 10;
    a = a + b;
    saveShared(a);

    a += 1

    return a;
}
```

Thread B

```c
main ()
{
    int a,b;

    a = getShared();
    b = 20;
    a = a - b;
    saveShared(a);

    a += 1

    return a;
}
```

A C B D E
Which code region is a critical section?

read + modify + write of shared variable

Thread A

```c
main ()
{
    int a, b;
    a = getShared();
    b = 10;
    a = a + b;
    saveShared(a);
    a += 1
    return a;
}
```

Thread B

```c
main ()
{
    int a, b;
    a = getShared();
    b = 20;
    a = a - b;
    saveShared(a);
    a += 1
    return a;
}
```

Large enough for correctness + Small enough to minimize slow down
Which values might the shared s variable hold after both threads finish?

Thread A

```c
main()
{
  int a, b;
  a = getShared();
  b = 10;
  a = a + b;
  saveShared(a);
  return a;
}
```

Thread B

```c
main()
{
  int a, b;
  a = getShared();
  b = 20;
  a = a - b;
  saveShared(a);
  return a;
}
```

s = 40;
If A runs first

Thread A

```c
main ()
{
    int a, b;
    a = getShared();
    b = 10;
    a = a + b;
    saveShared(a);
    return a;
}
```

(s = 40)

s = 50

Thread B

```c
main ()
{
    int a, b;
    a = getShared();
    b = 20;
    a = a - b;
    saveShared(a);
    return a;
}
```
B runs after A Completes

Thread A

```plaintext
main ()
{
  int a,b;
  a = getShared();
  b = 10;
  a = a + b;
  saveShared(a);
  return a;
}
```

(s = 50)

Thread B

```plaintext
main ()
{
  int a,b;
  a = getShared();
  b = 20;
  a = a - b;
  saveShared(a);
  return a;
}
```

(shared memory)

s = 30;
What about interleaving?

Thread A

```c
main ()
{ int a,b;
  a = getShared();
  b = 10;
  a = a + b;
  saveShared(a);
  return a;
}
```

Thread B

```c
main ()
{ int a,b;
  a = getShared();
  b = 20;
  a = a - b;
  saveShared(a);
  return a;
}
```

One of the threads will overwrite the other’s changes.
Four Rules for Mutual Exclusion

1. No two threads can be inside their critical sections at the same time (one of many but not more than one).
2. No thread outside its critical section may prevent others from entering their critical sections.
3. No thread should have to wait forever to enter its critical section. (Starvation)
4. No assumptions can be made about speeds or number of CPU’s.
Railroad Semaphore
- Help trains figure out which track to be on at any given time.
Railroad Semaphore
- Help trains figure out which track to be on at any given time.

O.S. Semaphore:
- Construct that the OS provides to processes.
- Make system calls to modify their value
Mutual Exclusion with Semaphores

mutex = 1; // lock and unlock mutex atomically.

T₀
lock (mutex);
< critical section >
unlock (mutex);

T₁
lock (mutex);
< critical section >
unlock (mutex);

Atomicity: run the entire instruction without interruption.
Mutual Exclusion with Semaphores

mutex = 1; //unlocked.

\textbf{T}_0 \quad \textbf{T}_1
\begin{align*}
\text{lock (mutex);} & \quad \text{lock (mutex);} \\
< \text{critical section} > & \quad < \text{critical section} > \\
\text{unlock (mutex);} & \quad \text{unlock (mutex)};
\end{align*}

Atomicity: run the entire instruction without interruption.

\textbf{T}_0: \quad \text{Wants to execute the critical section}
\textbf{T}_0: \quad \text{Reads the value of mutex,}
\quad \text{Changes the value of mutex = 0 (acquires lock)}
\quad \text{Enters critical section.}
Mutual Exclusion with Semaphores

mutex = 0; // locked.

\( T_0 \)
lock (mutex);
< critical section >
unlock (mutex);

\( T_1 \)
lock (mutex);
< critical section >
unlock (mutex);

Atomicity: run the entire instruction without interruption.

\( T_0 \): Wants to execute the critical section
\( T_0 \): Reads the value of mutex,
Changes the value of mutex = 0 (acquires lock)
Enter critical section.

Atomic Execution
Mutual Exclusion with Semaphores

mutex = 0; //locked.

T₀
lock (mutex);
< critical section >
unlock (mutex);

T₁ (blocked)
lock (mutex);
< critical section >
unlock (mutex);

Atomicity: run the entire instruction without interruption.

T₀: In the critical section
T₁: Wants to enter the critical section.
   Reads the value of mutex (mutex = 0)
   Cannot enter critical section.
   Blocked.
Mutual Exclusion with Semaphores

```
mutex = 0;  //locked.
```

\(T_0\)  
lock (mutex);
< critical section >
unlock (mutex);

\(T_1\) (blocked)
lock (mutex);
< critical section >
unlock (mutex);

Atomicity: run the entire instruction without interruption.

\(T_0\): Completes execution of critical section
Updates mutex value = 1. (release lock)
Mutual Exclusion with Semaphores

mutex = 1; // unlocked.

T₀
lock (mutex);
< critical section >
unlock (mutex);

T₁ (blocked)
lock (mutex);
< critical section >
unlock (mutex);

Atomicity: run the entire instruction without interruption.

T₀: Completes execution of critical section
    Updates mutex value = 1. (release lock)
Mutual Exclusion with Semaphores

mutex = 1;  //locked.

$T_0$
lock (mutex);
< critical section >
unlock (mutex);

$T_1$
lock (mutex);
< critical section >
unlock (mutex);

Atomicity: run the entire instruction without interruption.

$T_1$: Can now acquire lock atomically and
Enter the critical section
Mutual Exclusion with Semaphores

- Use a “mutex” semaphore initialized to 1
- Only one thread can enter critical section at a time.
- Simple, works for any number of threads

\[
\text{mutex} = 1; // lock and unlock mutex atomically.
\]

\[
\begin{align*}
T_0 & \quad \text{lock (mutex);} \\
& \quad < \text{critical section} > \\
& \quad \text{unlock (mutex);} \\
T_1 & \quad \text{lock (mutex);} \\
& \quad < \text{critical section} > \\
& \quad \text{unlock (mutex);} 
\end{align*}
\]

Atomicity: runs as an entire instruction or not at all.
Synchronization: More than Mutexes

• “I want to block a thread until something specific happens.”
  – Condition variable: wait for a condition to be true

• “I want all my threads to sync up at the same point.”
  – Barrier: wait for everyone to catch up.
Barriers

• Used to coordinate threads, but also other forms of concurrent execution.

• Often found in simulations that have discrete rounds. (e.g., game of life)
Barrier Example, N Threads

shared barrier b;

init_barrier(&b, N);

create_threads(N, func);

void *func(void *arg) {
    while (...) {
        compute_sim_round()
        barrier_wait(&b)
    }
}

T₀  T₁  T₂  T₃  T₄

Barrier (0 waiting)
shared barrier b;

init_barrier(&b, N);

create_threads(N, func);

void *func(void *arg) {
    while (...) {
        compute_sim_round()
        barrier_wait(&b)
    }
}

Threads make progress computing current round at different rates.
Barrier Example, N Threads

shared barrier b;

init_barrier(&b, N);

create_threads(N, func);

void *func(void *arg) {
    while (...) {
        compute_sim_round()
        barrier_wait(&b)
    }
}

Threads that make it to barrier must wait for all others to get there.
Barrier Example, N Threads

shared barrier b;

init_barrier(&b, N);

create_threads(N, func);

void *func(void *arg) {
    while (...) {
        compute_sim_round()
        barrier_wait(&b)
    }
}

Barrier allows threads to pass when N threads reach it.
Barrier Example, N Threads

shared barrier b;

init_barrier(&b, N);

create_threads(N, func);

void *func(void *arg) {
    while (...) {
        compute_sim_round()
        barrier_wait(&b)
    }
}

Threads compute next round, wait on barrier again, repeat...

Barrier (0 waiting)
Synchronization: More than Mutexes

• “I want all my threads to sync up at the same point.”
  – Barrier: wait for everyone to catch up.

• “I want to block a thread until something specific happens.”
  – Condition variable: wait for a condition to be true

• “I want my threads to share a critical section when they’re reading, but still safely write.”
  – Readers/writers lock: distinguish how lock is used
Synchronization: Beyond Mutexes
Message Passing

- Operating system mechanism for IPC
  - send (destination, message_buffer)
  - receive (source, message_buffer)
- Data transfer: in to and out of kernel message buffers
- Synchronization: can’t receive until message is sent
Additional Slides: Solution to the Race Condition
Solution with mutexes

Thread A

```c
main ()
{
    int a, b;
    a = getShared();
    b = 10;
    a = a + b;
    saveShared(a);
    return a;
}
```

Thread B

```c
main ()
{
    int a, b;
    a = getShared();
    b = 20;
    a = a - b;
    saveShared(a);
    return a;
}
```
Using Locks

Thread A

```c
main ()
{ int a,b;

    a = getShared();
    b = 10;
    a = a + b;
    saveShared(a);

    return a;
}
```

Thread B

```c
main ()
{ int a,b;

    a = getShared();
    b = 20;
    a = a - b;
    saveShared(a);

    return a;
}
```
Using Locks

Thread A

```c
main ()
{
    int a,b;
    
    acquire(l);
    a = getShared();
    b = 10;
    a = a + b;
    saveShared(a);
    release(l);
    
    return a;
}
```

Thread B

```c
main ()
{
    int a,b;
    
    acquire(l);
    a = getShared();
    b = 20;
    a = a - b;
    saveShared(a);
    release(l);
    
    return a;
}
```

`s = 40;  
Lock 1; 
shared memory 

Lock Held by: Nobody`
Using Locks

Thread A

```c
main ()
{
    int a, b;
    acquire(l);
    a = getShared();
    b = 10;
    a = a + b;
    saveShared(a);
    release(l);
    return a;
}
```

Thread B

```c
main ()
{
    int a, b;
    acquire(l);
    a = getShared();
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    release(l);
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s = 40;
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Lock held by:
Thread A
Using Locks

Thread A

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Thread B

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main ()
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  acquire(l);
  a = getShared();
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  release(l);

  return a;
}
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s = 40;
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Lock held by:
Thread A
Using Locks

Thread A

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Thread B

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main ()
{ int a, b;

    acquire(l);
    a = getShared();
    b = 20;
    a = a - b;
    saveShared(a);
    release(l);

    return a;
}
```

`s` = 40

Lock held by:
Thread A
Using Locks

Thread A

```c
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  release(l);

  return a;
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Thread B

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  b = 20;
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  return a;
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s = 40;  

Lock Held by:  

Nobody
Using Locks

Thread A

```c
main ()
{ int a,b;

    acquire(l);
    a = getShared();
    b = 10;
    a = a + b;
    saveShared(a);
    release(l);

    return a;
}
```

Thread B

```c
main ()
{ int a,b;

    acquire(l);
    a = getShared();
    b = 20;
    a = a - b;
    saveShared(a);
    release(l);

    return a;
}
```

Lock held by:
Thread B

---

s = 40;
Lock l;
Using Locks

Thread A

```c
main ()
{
    int a, b;

    acquire(l);
    a = getShared();
    b = 10;
    a = a + b;
    saveShared(a);
    release(l);

    return a;
}
```

Thread B

```c
main ()
{
    int a, b;

    acquire(l);
    a = getShared();
    b = 20;
    a = a - b;
    saveShared(a);
    release(l);

    return a;
}
```

```
s = 40;
Lock 1;
```

Lock Held by: Nobody
Using Locks

- No matter how we order threads or when we context switch, result will always be 30, like we expected (and probably wanted).