CS 31: Introduction to Computer Systems

22: Race Conditions & Synchronization April 23, 2020



Recap

- To speed up a job, must divide it across multiple cores.
- Thread: abstraction for execution within process.
 - Threads share process memory.
 - Threads may need to communicate to achieve goal
- Thread communication:
 - To solve task (e.g., neighbor GOL cells)
 - To prevent bad interactions (synchronization)

If one CPU core can run a program at a rate of X, how quickly will the program run on two cores? Why?

- A. Slower than one core (<X)
- B. The same speed (X)
- C. Faster than one core, but not double (X-2X)
- D. Twice as fast (2X)
- E. More than twice as fast(>2X)

If one CPU core can run a program at a rate of X, how quickly will the program run on two cores? Why?

- A. Slower than one core (<X) (if we try to parallelize serial applications!)
- B. The same speed (X) (some applications are not parallelizable)
- C. Faster than one core, but not double (X-2X): most of the time: (some communication overhead to coordinate/synchronization of the threads)
- D. Twice as fast (2X)(class of problems called embarrassingly parallel programs. E.g. protein folding, SETI)
- E. More than twice as fast(>2X) (rare: possible if you have more CPU + more memory)

Parallel Speedup

- Performance benefit of parallel threads depends on many factors:
 - algorithm divisibility
 - communication overhead
 - memory hierarchy and locality
 - implementation quality
- For most programs, more threads means more communication, diminishing returns.



Kernel-Level Threads



Kernel Context switching over threads

Each process has explicitly mapped regions for stacks

Synchronization

- Synchronize: to (arrange events to) happen such that two events do not overwrite each other's work.
- Thread synchronization
 - When one thread has to wait for another
 - Events in threads that occur "at the same time"
- Uses of synchronization
 - Prevent race conditions
 - Wait for resources to become available (only one thread has access at any time deadlocks)

Synchronization: Too Much Milk (TMM)

Time	You	Your Roommate
3.00	Arrive home	
3.05	Look in fridge, no milk	
3.10	Leave for the grocery store	
3.15		
3.20	Arrive at the grocery store	
3.25	Buy Milk	
3.30		
3.35	Arrive home, put milk in fridge	Arrive Home
3.40		Look in fridge, find milk
3.45		Cold Coffee (nom)



What mechanisms do we need for two independent threads to communicate and get a consistent view (computer state)?

How many cartons of milk can we have in this scenario? (Can we ensure this somehow?)

Time	You	Your Roommate
3.00	Arrive home	
3.05	Look in fridge, no milk	
3.10	Leave for the grocery store	
3.15		
3.20	Arrive at the grocery store	
3.25	Buy Milk	
3.30		
3.35	Arrive home, put milk in fridge	Arrive Home
3.40		Look in fridge, find milk
3.45		Cold Coffee (nom)



- A. One carton (you)
- B. Two cartons
- C. No cartons
- D. Something else

Synchronization: Too Much Milk (TMM): One possible scenario

Time	You	Your Roommate
3.00	Arrive home	
3.05	Look in fridge, no milk	
3.10	Leave for grocery	Arrive Home
3.15		Look in fridge, no milk
3.20	Arrive at grocery	Leave for grocery
3.25	Buy Milk	
3.30		Arrive at grocery
3.35	Arrive home, put milk in fridge	
3.40		Arrive home, put milk in fridge
3.45		Oh No!



What mechanisms do we need for two independent threads to communicate and get a consistent view (computer state)?

Lecture 8 – Slide-11

Synchronization:

<u>Threads get scheduled in an arbitrary manner:</u> bad things may happen: ...or nothing may happen

Time	You	Your Roommate
3.00	Arrive home	
3.05	Look in fridge, no milk	
3.10	Leave for grocery	Arrive Home
3.15		Look in fridge, no milk
3.20	Arrive at grocery	Leave for grocery
3.25	Buy Milk	
3.30		Arrive at grocery
3.35	Arrive home, put milk in fridge	
3.40		Arrive home, put milk in fridge
3.45		Oh No!



What mechanisms do we need for two independent threads to communicate and get a consistent view (computer state)?

Synchronization Example



- Coordination required:
 - Which thread goes first?
 - Threads in different regions must work together to compute new value for boundary cells.
 - Threads might not run at the same speed (depends on the OS scheduler). Can't let one region get too far ahead.
 - Context switches can happen at any time!

Thread Ordering

(Why threads require care. Humans aren't good at reasoning about this.)

- As a programmer you have *no idea* when threads will run. The OS schedules them, and the schedule will vary across runs.
- It might decide to context switch from one thread to another *at any time*.
- Your code must be prepared for this!
 - Ask yourself: "<u>Would something bad happen if we context</u> <u>switched here?</u>"
- hard to debug this problem if it is not reproducible

Example: The Credit/Debit Problem

- Say you have \$1000 in your bank account
 - You deposit \$100
 - You also withdraw \$100
- How much should be in your account?
- What if your deposit and withdrawal occur at the same time, at different ATMs?

```
Thread T<sub>0</sub>
Credit (int a) {
    int b;
    b = ReadBalance ();
    b = b + a;
    WriteBalance (b);
    PrintReceipt (b);
}
```

Thread T_1

}

```
Debit (int a) {
    int b;
```

```
b = ReadBalance ();
b = b - a;
WriteBalance (b);
```

```
PrintReceipt (b);
```



}

Thread T_1

Debit (int a) { int b;

> b = ReadBalance ();b = b - a;WriteBalance (b);

PrintReceipt (b);

	Say T _o runs first	
	Read \$1000 into b	
Thread T ₀	Switch to T ₁ Read \$1000 into b	Thread T ₁
Credit (int a) { int b;	Debit by \$100 Write \$900	Debit (int a) { int b;
<pre>b = ReadBalance (); b = b + a; WriteBalance (b);</pre>		<pre>b = ReadBalance (); b = b - a; WriteBalance (b);</pre>
<pre>PrintReceipt (b); }</pre>		<pre>PrintReceipt (b); }</pre>

CONTEXT SWITCH

	Say T _o runs first	
	Read \$1000 into b	
Thread T _o	Switch to T_1	Thread T_1
Credit (int a) { int b;	Debit by \$100 Write \$900	Debit (int a) { int b;
<pre>b = ReadBalance (); b = b + a; WriteBalance (b);</pre>		b = ReadBalance (); b = b − a; WriteBalance (b);←
<pre>PrintReceipt (b); }</pre>		<pre>PrintReceipt (b); }</pre>
	Switch back to T ₀ Read \$1000 into b	Bank gave you \$100!
	Credit \$100 Write \$1100	What went wrong?

"Critical Section"



To Avoid Race Conditions



- 1. Identify critical sections
- 2. Use synchronization to enforce mutual exclusion
 - Only one thread active in a critical section

Critical Section and Atomicity

- Sections of code executed by multiple threads
 - Access shared variables, often making local copy
 - Places where order of execution or <u>thread interleaving</u> will affect the outcome
 - Follows: read + modify + write of shared variable
- Must run atomically with respect to each other
 - <u>Atomicity</u>: runs as an entire instruction or not at all.
 Cannot be divided into smaller parts.

Which code region is a critical section?



Thread B

Which code region is a critical section? read + modify + write of shared variable



Large enough for correctness + Small enough to minimize slow down

Which values might the shared s variable hold after both threads finish?

shared

memory

s = 40;

Thread A

main ()	
{ int a,b;	
a = getShared();	
b = 10;	
a = a + b;	
<pre>saveShared(a);</pre>	
return a;	
}	

Thread B

main ()
{ int a,b;
a = getShared();
b = 20;
a = a - b;
<pre>saveShared(a);</pre>
return a;
}

If A runs first

Thread A



Thread B

B runs after A Completes



What about interleaving?



One of the threads will overwrite the other's changes.

Four Rules for Mutual Exclusion

- No two threads can be inside their critical sections at the same time (one of many but not more than one).
- 2. No thread outside its critical section may prevent others from entering their critical sections.
- **3.** No thread should have to wait forever to enter its critical section. (Starvation)
- 4. No assumptions can be made about speeds or number of CPU's.



Railroad Semaphore

 Help trains figure out which track to be on at any given time.



Railroad Semaphore

 Help trains figure out which track to be on at any given time.

O.S. Semaphore:

- Construct that the OS provides to processes.
- Make system calls to modify their value

mutex = 1; //lock and unlock mutex atomically.

Τ ₀	\mathtt{T}_1
lock (mutex);	lock (mutex);
< critical section >	< critical section >
unlock (mutex);	unlock (mutex);

<u>Atomicity</u>: run the entire instruction without interruption.



Atomicity: run the entire instruction without interruption.

```
\mathbf{T}_0: Wants to execute the critical section
\mathbf{T}_0: Reads the value of mutex,
Changes the value of mutex = 0 (acquires lock)
Enters critical section.
```

<pre>mutex = 0; //locked.</pre>	
Το	T ₁
lock (mutex);	lock (mutex);
< critical section >	< critical section >
unlock (mutex);	unlock (mutex);

Atomicity: run the entire instruction without interruption.



<pre>mutex = 0; //locked.</pre>		
Τ ₀	T₁ (blocked)	
lock (mutex);	lock (mutex);	
< critical section >	< critical section >	
unlock (mutex);	unlock (mutex);	

Atomicity: run the entire instruction without interruption.

<pre>mutex = 0; //locked.</pre>		
Τ ₀	T ₁ (blocked)	
lock (mutex);	lock (mutex);	•
< critical section >	< critical section >	
unlock (mutex);	unlock (mutex);	

Atomicity: run the entire instruction without interruption.

 T_0 : Completes execution of critical section Updates mutex value = 1. (release lock)



Atomicity: run the entire instruction without interruption.

 $\mathbf{T}_{0}: \text{ Completes execution of critical section} \\ \text{Updates mutex value = 1. (release lock)} \\ \text{Atomic Execution} \\ \text{Atomi$



Atomicity: run the entire instruction without interruption.

 $\mathbf{T_1}\colon$ Can now acquire lock atomically and Enter the critical section

mutex = 1; //lock and unlock mutex atomically.

Τ ₀	T ₁
lock (mutex);	lock (mutex);
< critical section >	< critical section >
unlock (mutex);	unlock (mutex);

- Use a "mutex" semaphore initialized to 1
- Only one thread can enter critical section at a time.
- Simple, works for any number of threads

Atomicity: runs as an entire instruction or not at all.

Synchronization: More than Mutexes

• "I want to block a thread until something specific happens."

Condition variable: wait for a condition to be true

- "I want all my threads to sync up at the same point."
 - Barrier: wait for everyone to catch up.

Barriers

- Used to coordinate threads, but also other forms of concurrent execution.
- Often found in simulations that have discrete rounds.
 (e.g., game of life)

```
shared barrier b;
```

```
init_barrier(&b, N);
```

```
create_threads(N, func);
```

```
void *func(void *arg) {
  while (...) {
    compute_sim_round()
    barrier_wait(&b)
```

}



Barrier (0 waiting)

```
shared barrier b;
init barrier(&b, N);
                                        T_1
create threads (N, func);
                                     \mathsf{T}_0
void *func(void *arg) {
  while (...) {
                                        Barrier (0 waiting)
     compute sim round()
    barrier wait(&b)
  }
```

Threads make progress computing current round at different rates.

 T_2

 T_3

 T_4

Time

Time

```
Threads that make it to barrier must
shared barrier b;
                                         wait for all others to get there.
init barrier(&b, N);
                                                 \mathsf{T}_1
create threads (N, func);
                                                          T_3
void *func(void *arg) {
                                            T_0
                                                     T_2
                                                              Τ<sub>Δ</sub>
  while (...) {
                                                Barrier (3 waiting)
      compute sim round()
     barrier wait(&b)
   }
```



Time

 T_4

 T_3

```
Threads compute next round, wait
shared barrier b;
                                         on barrier again, repeat...
init barrier(&b, N);
create threads (N, func);
void *func(void *arg) {
  while (...) {
                                                Barrier (0 waiting)
      compute sim round()
                                             \mathsf{T}_0
                                                      1<sub>2</sub>
                                                 T<sub>1</sub>
     barrier wait(&b)
   }
```

Synchronization: More than Mutexes

- "I want all my threads to sync up at the same point."
 Barrier: wait for everyone to catch up.
- "I want to block a thread until something specific happens."
 - Condition variable: wait for a condition to be true
- "I want my threads to share a critical section when they're reading, but still safely write."
 - Readers/writers lock: distinguish how lock is used

Synchronization: Beyond Mutexes Message Passing



- Operating system mechanism for IPC
 - send (destination, message_buffer)
 receive (source, message buffer)
- Data transfer: in to and out of kernel message buffers
- Synchronization: can't receive until message is sent

Additional Slides: Solution to the Race Condition

Solution with mutexes

Thread A



Thread B

Thread A

main ()		main ()
{ int a,b;		{ int a,b;
a = getShared();		<pre>a = getShared();</pre>
b = 10;		b = 20;
a = a + b;		a = a - b;
saveShared(a);	shared	<pre>saveShared(a);</pre>
	memory	
return a:	s = 40:	return a;
}		}
, ,		

Thread B

Thread B



Thread B



Thread B



Thread B



Thread B



Thread B



Thread B



Thread B

Thread A



 No matter how we order threads or when we context switch, result will always be 30, like we expected (and probably wanted).