



5. Be sure to show your work for each question. Given the following 8-bit binary values:

value1: 0b01011101

value2: 0b01100101

- (a) What is the decimal representation of the resulting addition if the two values are interpreted as 8-bit *unsigned* values?
- (b) What is the decimal representation of the resulting addition if the two values are interpreted as 8-bit *signed* values?
- (c) What is the binary representation of the result of adding the two values together? Does this operation result in overflow when the values are interpreted as *unsigned*, *signed*, both, or neither? Justify your answer.
- (d) What is the binary representation of the result of subtracting the second from the first (value1 - value2)? Does this operation result in overflow when the values are interpreted as *unsigned*, *signed*, both, or neither? Justify your answer.

6. Convert the following 2-byte binary numbers directly to hexadecimal, without converting to decimal first. Recall that one hexadecimal digit corresponds to 4 bits. (The binary values have spaces between each set of 4 bits to make them more readable.)

(a) 0000 0110 0001 1111

(b) 1100 0101 1110 0101

(c) 1010 0111 1101 0110

7. Convert the following hexadecimal numbers to **2-byte** binary, without converting to decimal first. Recall that one hexadecimal digit corresponds to 4 bits.

(a) 0x23

(b) 0x852

(c) 0xc1a6

(d) 0xefab

8. Convert the following decimal values to 8-bit *signed* (two's complement) **binary** and then convert your binary result into **hexadecimal**. Show your work.

(a) 12

(b) -36

(c) 123

(d) -123

9. Given the following 4-bit binary values, show the results of each bit-wise operation, showing both the binary and decimal result value for each (list the unsigned decimal value):

(a)  $0110 \mid \sim(1010)$

(b)  $\sim(0110 \mid 1010)$

(c)  $0111 \& \sim(1001)$

(d)  $(1010 \mid 0000) \& 1111$

(e)  $0011 \wedge 1110$

(f)  $0111 \ll 2$

(g)  $0111 \gg 2$