CS31 Worksheet: Week 3 Digital Circuits

Q1. Bitwise operations

- 0101 & 1101
- 0101 | 1101

Logical (unsigned) bit shift:

- 1010 << 2
- 1010 >> 2

Arithmetic (signed) bit shift:

- 1010 << 2
- 1010 >> 2

Q2. What does this circuit output?







Clicker Choices

X	Y	Out _A	Out _B	Out _c	Out _D	Out _E				
0	0	0	1	0	1	0				
0	1	0	1	0	0	1				
1	0	1	0	1	1	1				
1	1	0	0	1	1	0				

Q3. Using AND, OR and NOT gates, draw out an XOR Circuit



A	В	Sum (A + B)	C _{out}	
0	0	0	0	
0	1	1	0	
1	0	1	0	
1	1	0	1	

Q4. Given the following truth table, draw out a one-bit adder.

Q5. Let's now draw out a one-bit full adder

Write Boolean expressions	А	в	C_{in}	Sum	C_{out}
for Sum = 1 and $C_{\text{out}} = 1$	0	0	0	0	0
out out out	0	1	0	1	0
	1	0	0	1	0
	1	1	0	0	1
	0	0	1	1	0
	0	1	1	0	1
	1	0	1	0	1
When is Sum 1?	1	1	1	1	1

When is C_{out} 1?



